

Abstract

The invention relates to an integrated semiconductor apparatus with trimmable generators (G_A, G_B, G_X). The trimming data (TRM<i>), which is stored in a fuse block device (10), is transmitted in serial form, in the form of a shift register chain, from the fuse block device (10) to trimming units of the generators (G_A, G_B, G_X).

(Figure 1)